



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	Ramesh V. Peri et al.	§	Art Unit:	2186
Serial No.:	10/717,085	§	Examiner:	Pierre Miche Bataille
Filed:	November 19, 2003	§	Docket:	ITL.1059US P17918
For:	Accessing Data From Different Memory Locations in the Same Cycle	§	Assignee:	Intel Corporation

Mail Stop **RCE**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to the receipt of an office action, please amend the above-referenced patent application as follows:

Date of Deposit: June 25, 2008
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
Cynthia L. Hayden
Cynthia L. Hayden